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DATA SHEET

MLCC Chip Capacitor Size 2225

Serie:

Mat. Code Y5V
Voltage Code 250
Range Code 334

DRW: Jason CHKD Wilson

APPD: Schumi www.edcon-components.com Copyright by EDCON-COMPONENTS EDCON-COMPONENTS

Structure of Chip Capacitor

Nominal Capacitance Unit (pf)

Ordering Code Actual Value

0P5 0,5pf Note: The first two digits are 1R0 1.0pf

1,0pf 102 1000pf 224 220000pf

Capacitance Tolerance

Ord. Code В D F С G 0,1pf 0,25pf 0,5pf 0,01 Tolerance

0,02

Note: These capacitance tolerance B,C,D are just applicable th capacitance that eqals to or less.

Dielectric Style

Dielectric Code CG HG LG PΗ RH

Dielectric **COG NPO** HG LG PΗ RH

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Terminal Material Styles

Ordering Code **Termination Material**

(Silver Solderable Termination) S Ċ (Copper Solderable Termination) Ν (Nickel Barrier Termination)

Temperature Coefficient / Characteristics

Dielectric	Temperature Coefficient	Temperature I
COG/NPO	. 0 +/- 30 ppm/°C	.+20°C > -55°(
HG	33 +/-30 ppm/°C	.+20°C > -55°(
LG	75 +/-30 ppm/°C	.+20°C > -55°(
CH	0 +/-60 ppm/°C	.+20°C > -55°(
PH	150 +/-60 ppm/°C	.+20°C > -55°(
RH	220 +/-60 ppm/°C	.+20°C > -55°(
SH	330 +/-60 ppm/°C	.+20°C > -55°(
TH	470 +/-60 ppm/°C	.+20°C > -55°(
UJ	750 +/-120 ppm/°C	.+20°C > -55°(
SL	1000 +140 ppm/°C	.+20°C > -55°(
X7R	. +/-15%	.+20°C > -55°(
X5R	. +/-15%	.+20°C > -55°(
Z5U	56% ~ +22%	.+20°C > -55°(
Y5V	80% ~ +30%	.+20°C > -55°(

Note: Nominal Temperature coefficient and allowed tolerance of class 1 are decided by the changing

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CHKD Wilson

Note for CF $\label{eq:confirmed}$ The following Q value is just confirmed by general customer. If there is a higher requirement for Q value $\ \ \, = \ \, (1-r)^{-1} \,$

For the customer whose requirements for frequency is between 1MHz and 2,4GHz or higher frequen

Capacitance (pf)	Q value	at 300MHz	Cap	Capacitance (pf) Q value	
	O805	O603			O805
4,7		400	320	20	90
5,2		36	288	22	86
5,6		340	272	24	80
6,2		320	256	27	70
6,8		280	224	30	60
7,5		260	208	33	56
8,2		230	184	36	52
9,1		210	168	39	48
10		200	160	43	44
11		180	144	47	40
12		160	128	51	36
13		150	120	56	34
14		140	112	62	32
15		130	104	68	30
16		120	96	75	28
18		100	80	82	26

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Wilson

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Reliability Test

Item Technical specification

Capacitance Class I Should be within the specifie

DF, tan Dissipation Favctor Class II Should be within the specifie

Class I DF≤ 0,15%

Class II X7R

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Reliability Test

DF, tan Dissipation Favctor Class II Y5V / Z5U

(IR) Insulation Resistance Class I C≤ 10nf, Ri \geq 50000M Ω

C> 10nf, Ri ≥ 500S

Class II X7R

Y5V / Z5U

Item (DWV) Dielectric Withstanding Voltage

Technical Specification No Breakdown or damage

At least 95% of the terminal electrode is covered ba new

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Reliability Test

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Resistance to Soldering Heat Item NPO to SL

C/C ≤ 0,5%

DF Same to initial Value IR Same to initial Value

Apperance: No visible damage. At least 95% of the termin

Resistance to Flexure of Substrate (Bend Apperance: No visible damage.

C/C ≤ +/- 10%

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Reliability Test

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ItemTechnical specificationTermination AdhesionNo visible damage

Temperature Cycle Class I : ≤+/-1% or 1pf whichever is larger. Class

Moisture Resistance C/C Class I \leq +/-2% or 1pf whichever is larger

Class II B: \leq +/-10% Class II E,F: \leq +/-30%

DF Not more than twice of initial value

IR Class I: Ri≥2500MΩ Ri/Cr ≥25sec whichev

Class II: Ri≥1000MΩ Ri/Cr ≥25sec whichev

Wilson

Visual Apperance: No visible damage

Note: Pretratment (only for class 2 capacitor)

Pretradment (only for class 2 capacitor) is a method to treat the capacitor before measurement. First up-category temperasture or other specified higher temperature environment for 1 hour. Then recove pressure conditions for 24hours +/-1hrs..

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Package Conditions

Dimension of paper taping for size 0402

С Code W1 L1 D В 0402 1,15 8,0 3,5 1,75 0,65 ±0,20 ±0,20 ±0,20 ±0,05 ±0,10

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Package Conditions

Dimension of paper taping for size $0805 \sim 1812$

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Package Conditions

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Dimension of Reel (mm)

	A B	С	D	E
7`Reel	\emptyset 178 ± 2,0	$3 \ \emptyset 13 \ \pm 0.5$	Ø21 ± 0.8	Ø50 or more
40`D!	Ø000 : 0 0	0.640 . 0.5	Ø04 . 0.0	Ø50
13`Reel	Ø330± 2,0	$3 \emptyset 13 \pm 0.5$	Ø21 ± 0,8	שטט or more

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Packing Quantity

	Packing Style Quanti	ty	
Size	PT	ET	BC
0402	10000		20000
0603	5000		15000
0805	5000	2500	10000
1206	5000	2500	5000
1210		2000	
1808		2000	
1812		2000	
2225			
3035			

Note: We can choose packing style and quantity can be according to the customer requirement

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Soldering Profile Curve			
DRW: APPD: www.edcon-components.cc Copyright by EDCON-COM EDCON-COMPONENTS	Jason Schumi om PONENTS	CHKD	Wilson

Ordering Information

Serie Range Material

I11010 - 334 Y5V

MLCC Chip Capacitor Size 2225 334= 330000pf Y5V= Y5V Material

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l11010	
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Material:

Voltage:

Range:

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TOLERANCE

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No Name

- 1 Ceramic dielectric
- 2 Inner Electrode
- 3 Substrate Electrode
- 4 Nickel Layer
- 5 Tin Layer

J	0,05	K 5	0,1	M	0,2	S .+50% / -20%	Z .+80% / -20%		
SH		тн		UJ		SL	X	В	E
SH		TH		UJ		SL	X5R	X7R	Z5U
		MATL: FINISH				Wilson Jamy		TOLERANCE	edcon-compone
								Application	
								Hi-Q COG ca	pacitance are i
								Note for CQ	
								The following	Q value is just
Point C > +20° C > +20° C > +20° C > +20°	°C >+	-85°C -85°C						For the custo	mer whose req
C > +20° C > +20°	°C >+	-85°C -85°C						Capacitance	(pf)
C > +20° C > +20°	+< 2° +< 2° +< 2° +< 2° +< 2° +< 2°	-85°C -85°C -85°C -125°C -85°C -85°C						4,7 5,2 5,6 6,2 6,8 7,5 8,2	
g of the c	apac	citance betv	weer	n 20°C and	l 85°	С		9,1 10	

MATL: Wilson FINISH Jamy

TOLERANCE

email: info@edcon-compone

alue requirements, we can design and produce according to the special requirements.

ncy, we can design it according to theri requierements. The frequency of CQ is a little bit higher than t

SHMC	Capad	citance (pf)	ce (pf) Q value at 300MHz		
O603	72	91	O805	O603 24	20
	69	100		22	18
	64	110		20	16
	56	120		28	15
	48	130		16	13
	45				
	42				
	39				
	36				
	32				
	29				
	28				
	26				
	24				
	23				
	20				
	MATL FINIS		Wilson Jamy		TOLERANCE
	1 11410	1 1	Jamy		email: info@edcon-compone

Test Methods and Remarks

ed tolerance Capacitance

≤1000pf ≥1000pf C≤10µF

ed tolerance C≤10µF

C>10µF

Z5U

Capacitance ≤1000pf ≥1000pf

>50V 25V 16V 10V $\leq 2,5\%$ $\leq 3,5\%$ $\leq 3,5\%$ $\leq 5\%$

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≥25V 16V 10V ≤ 7,0% ≤12,5% ≤12,5%

(C< 1,0µF) ≤ 9,0% (C≥ 1,0µF)

C≤ 25nf, Ri ≥10000M Ω C> 25nf, Ri ≥ 100S C≤ 25nf, Ri ≥4000M Ω

solder. Visual Appearance: No visible damage:

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TOLERANCE

X7R Y5V Z5U . -5 ~ +10% . -10 ~ +20%

Preheating co Solder Tempe Clean the cap

nal electrode is covered by new solder.

Recovery time Recovery cond

Test Board: Al Wrap: 1mm Speed 0,5mm Unit: mm The measurer MATL: FINISH Wilson

Jamy

TOLERANCE

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II: $B = \le +/-10\%$ $E,F: \le +/-20\%$

Test Methods and Remarks

Applied Force: 5N

Duration: 10sec +/- 1sec

Preheating conditions: up-category temperations

Recovery time: 24hrs +/-1hrs

Initial Measurement

Cycling Tiems 5times, 1cycle, 4steps

Recovery time after test 24hrs +/- 2hrs

Temperature: 40°C +/-2°C Humidity: 90~95% RH Voltage: Rated Voltage Duration: 500hrs

Charge/Discharge Current: 50mA max. Recovery Time; 24hrs (Class I) or 48hrs (C

er is smaller

ver is smaller

t place the capacitor in the ery the capacitor at standard

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TOLERANCE

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Paper Size Size Code 0603

0805

1206

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email: info@edcon-compone

Paper Size Size Code 0805

1210 1808

1812

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Taping Specifi

F G \emptyset 10 ± 1,5 12 max.

Ø10 $\pm 1,5$ 12 max.

ΒP

2000 500 Standard: 0,1N < peeling stg No paper dirty remains on the

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Soldering Informations

Storage Methods
The guaranteed period for so
5000
Storage conditions:
Temperature: 5~40°C
5000
Relative Humidity: 20~70%
5000
Precausions for use
2000
The Multilayer Ceramic Capa

Following precautions for sal

Manual Soldering
Manual Soldering can pose a

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Voltage Capacitance Tolerance Termination Material No Function

250	ı	N	N
/5U	-1	IN .	IN IN

250= 25Volt J= Tol. 5% S= Silver Termination N= No Functic

G= Tol. 2% C= Copper Termination

F= Tol. 1% N= Nickel Termination

D= Tol. 0,5pf

C= Tol. 0,25pf

B= Tol. 0,1pf

Tol.Code B,C,D are only for ≤10pf

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Y5V= Y5V Material

250= 25Volt

334= 330000pf

MLCC Chip C

Mason DATE 40578 Customer: Sheet No. 1 from 14

Sheet No. ents.com

Drawing

Dimensions (mm)

 Chip-Size Code
 L
 W
 T

 2225
 5,7
 6,3 ≤2,50

Note: We can design according to customer special requirements

Rated Voltage (V)

	Code 6V3	500 201 102	tual Value 6,3 50 200 1000	Note: `	The first two digits are
F Y5V					MLCC Chip C
Mason Sheet No. ents.com	DATE		2 fro	40578 m 14	Part No.: Customer:

deally suited for RF and Microwave application requiring high Q, low ESR, and high resonant frequen confirmed by general customer. If there is a higher requirement for Q value requirements, we can de uirements for frequency is between 1MHz and 2,4GHz or higher frequency, we can design it according

Q value	value at 300MHz		apacitance (pf)	Q value	at 300MHz	Ca	apacitance (
O805	O603			O805	O603		
	1000	800	11		450	360	24
	900	720	12		400	320	27
	850	680	13		375	300	30
	800	640	14		350	280	33
	700	560	15		325	260	36
	650	20	16		300	240	39
	575	460	18		250	200	43
	525	420	20		225	180	47
	500	400	22		215	172	

Mason DATE 40578 Customer: Sheet No. 3 from 14

ents.com

High Voltage MLCC

Middle & High Voltage MLCC is a kind of special design MLCC that bases on the technology of gene

hat of CF. Please choose them according to your requirements.

Application

Analog & Digital modems LAN & WAN Inteface Lighting Ballast Circuits Voltage Multipliers DC-DC-Converter Back-Lighting Inverters

Measurement Method for High Voltage MLCC

Rated Voltage Range Measuring Method

500V ≤Vr ≤ 1000V Force 150% Rated Voltage for 5seconds. Max. current sh

1000V ≤Vr ≤ 2000V Force 120% Rated Voltage for 5seconds. Max. current sh

2000V ≤Vr ≤ 5000V Force 120% Rated Voltage for 5seconds. Max. current sh

MLCC Chip C

Part No.:

Mason DATE 40578 Customer:

Sheet No. 4 from 14

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Measuring Frequency

Measuring Voltage

1,0 Tol. 0,2Vrms

1MHz +/- 10% 1KHz +/- 10%

Test Frequency 1KHZ +/-10% Test Voltage 1,0 +/- 0,2Vrms

X7R, Y5V

Test Frequency 1KHZ +/-10% Test Voltage 1,0 +/- 0,2Vrms

Z5U

Test Frequency 1KHZ +/-10% Test Voltage 1,0 +/- 0,2Vrms

Measuring Frequency Measuring Voltage 1MHz +/- 10% 1,0 Tol. 0,2Vrms

1KHz +/- 10%

6,3V C= $10\mu F$

≤ 5% Test Frequency 1KHZ +/-10%

 $(C < 3,3\mu F)$

≤ 10% Test Voltage 1,0 +/- 0,2Vrms

(C≥ 3,3µF)

MLCC Chip C

Part No.: Customer:

 Mason
 DATE
 40578

 Sheet No.
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 $C=10\mu F\ X7R;\ Y5V$

12,5% Test Frequency 120Hz +/-20Hz
Test Voltage 0.5 +/- 0.1Vrms

Test Voltage 0,5 +/- 0,1Vrms
Test Frequency 0,1KHz
Test Voltage 0,5 +/- 0,05Vrms

Measuring Voltage: Rated Voltage Duration: 60Sec. +/-

6,3V ≤12,5% Test Method and Remarks

Measuring Voltage:

Class I: 300% Rated Voltage Class II: 250% Rated Voltage

Duration: 5 +/-1sec

Charge / Discharge Current : 50mA max. This method excludes high voltage MLCC Solder Temperature: 235°C +/- 5°C

Duration: 2 +/-0,5sec

MLCC Chip C

Part No.:

Customer:

DATE 40578 6 from 14

Sheet No. ents.com

Mason

nditions: 100 to 200°C +/- 10°C 2mon.

rature: 265°C +/- 5°C Duration 5sec. +/- 1sec.

acitor with solvent and examine it with a 10x(min) microscope.

eg: 24hrs +/-2hrs

ditions: Room temperature

12O3 or PCB

ı/sec.

ment should be mader with the board in bending position.

 Mason
 DATE
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 Sheet No.
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Part No.: Customer:

ature 1hrs

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(°C) temp. (NPO / X7R / Y5V / Z5U) . (+20) emp. (NPO / X7R / Y5V / Z5U) . (+20)

Class II)

MLCC Chip C

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Dimension of paper taping for size 0603, 0805, 1206

Α	В	С	D	Е	F	
	1,1	1,9	8	3,5	1,75	4
±0,20	±0,20	±0,20	±0,05	±0,10	±0,10	
	1,45	2,3	8	3,5	1,75	4
±0,20	±0,20	±0,20	±0,05	±0,10	±0,10	
	1,8	3,4	8	3,5	1,75	4
±0,20	±0,20	±0,20	±0,05	±0,10	±0,10	

MLCC Chip C

			Part No.:
Mason	DATE	40578	Customer:
Sheet No.		9 from 14	
ents.com			

Α	В	С	D	Е	F	
	1,55	2,35	8	3,5	1,75	4
±0,20	±0,20	±0,20	±0,05	±0,10	±0,10	
	1,95	3,6	8	3,5	1,75	4
±0.20	±0.20	±0.20	±0.05	±0.10	±0.10	

	2,7	3,42	8	3,5	1,75	4
±0,10	±0,10	±0,10	±0,05	±0,10	±0,10	
	2,2	4,95	12	5,5	1,75	4
±0,10	±0,10	±0,10	±0,05	±0,10	±0,10	
	3,66	4,95	12	5,5	1,75	8
±0,10	±0,10	±0,10	±0,05	±0,10	±0,10	

MLCC Chip C

Part No.: Mason DATE 40578 Customer: Sheet No. 10 from 14

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ication

jrength < 0,7N he scotch when peeling, and sticks to top an bottem tape							
			MLCC Chip C				
Mason Sheet No. ∍nts.com	DATE	40578 11 from 14	Part No.: Customer:				
olderability is 6 mounth (Under deliver package conditions).							
acitor (MLCC) may fail in a short circuit modern in an open circuit mode when subjected to severe co							
fety and Apllication Notes shall be taken in your major consideration. If you have a question about the							
a great risk of ceramic thermal cracks in capacitos. The hot soldering iron tip comes into direct contact							
			MLCC Chip C				
Mason Sheet No. ents.com	DATE	40578 12 from 14	Part No.: Customer:				

Too much solder Cracks tend occur due to large stress

Not enought solder Weak holding force may cause bad. connection between the capacitor and PCE

MLCC Chip C

Mason Sheet No. ents.com DATE

40578 13 from 14 Part No.: Customer:

No Function ROHS Packing

N R TR

on N= No Function R= Rohs Conform TR= Tape / Reel

N= NON Rohs Conform BU= Bulk Ware

MLCC Chip C

Mason DATE 40578 Customer:

Sheet No. 14 from 14 ents.com

apacitor Size 2225

I11010

WB

1

significant; third digit denotes number of zeros after range; P= decimal point

apacitor Size 2225

I11010

су

ssign and produce according to the special requirements.

ng to theri requierements. The frequency of CQ is a little bit higher than that of CF. Please choose the

pf) Q value at 300MHz

O805	O60	3
	200	160
	175	140
	150	120
	140	112
	130	104
	120	96
	110	88
	100	80

apacitor Size 2225

I11010	

Fral MLCC. This kind of MLCC has stable high voltage reliability and suitable to SMT. Middle & igh Vo

nould not exceed 50mA nould not exceed 50mA nould not exceed 10mA apacitor Size 2225

I11010

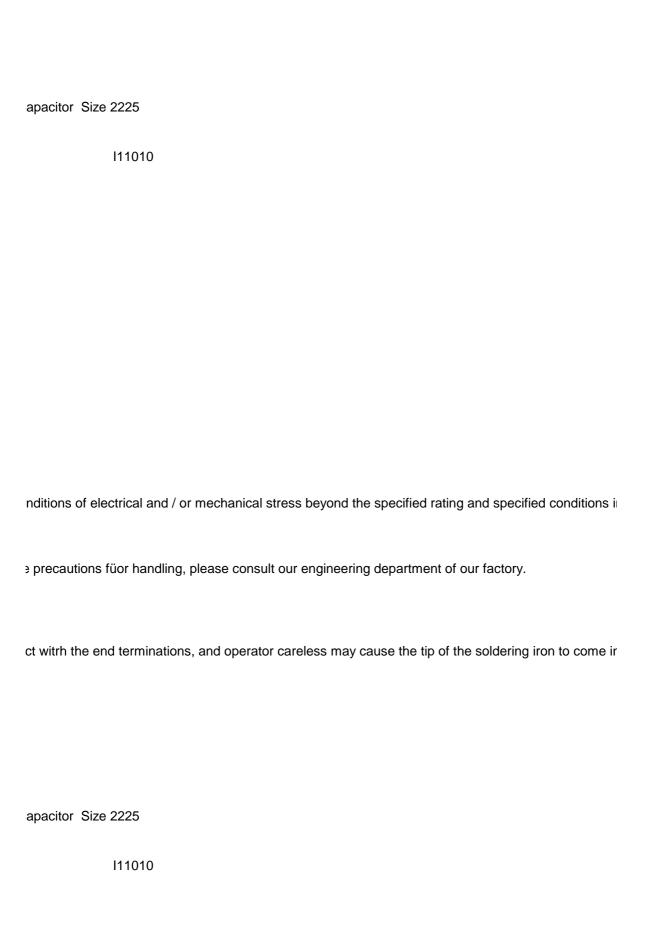
I11010

Time (min) 30 2~3 30 2~3

apacitor Size 2225

G	Н	J	Т	
	2	4	1,5	1,1
±0,10	±0,10	±0,10	below	
	2	4	1,5	1,1
±0,10	±0,10	±0,10	below	
	2	4	1,5	1,1
±0.10	±0.10	±0.10	below	

	2	4	1,55	1,55
±0,10	±0,10	±0,10	±0,10	
	2	4	1,5	1,8
±0,10	±0,10	±0,10	±0,10	
	2	4	1,55	1,85
±0,10	±0,10	±0,10	±0,10	



3

apacitor Size 2225





