



# DATA SHEET

## CHIP Low Leakage Current

### Serie: I15004

Voltage: 6,3Volt

Range: 22 $\mu$ F

Impedance: 23,5 $\Omega$

Dimension 4x5,4mm

Ripple Current: 31mA

**CHIP Low Leakage Current**

Serie No.: **I15004**

Customer:

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014
APPD:	Schumi			FINISH	Jamy		Sheet No.	1 from 7	

# EDCON-COMPONENTS



Designed for surface mounting on high density circuit board

Emboss carrier tape packing systems is available for automatic insertion



## Technical Informations

Voltage:	<b>6,3Volt</b>
Range;	<b>22µF</b>
Dimension; D x L mm	<b>4x5,4mm</b>
Impedance (Ω) max., at20°C, 100hKz	<b>23,5Ω</b>
Ripple Current (mA rms) at 105°C, 100kHz	<b>31mA</b>

Ordering Code			
<b>6V3</b>	<b>Voltage</b>		
<b>220</b>	<b>Range</b>		
<b>D=</b>	<b>A</b>	<b>L=</b>	<b>0</b>

Low Leakgae current ( 0,5µA to 3,3µA max).  
 Low cost for replacement of some tantalum applications  
 Low Impedance with Operating Temperatur Range of -40°C ~ +85°C  
 Load Life of 2000hours

<b>Leakage current max.</b>	I = 0,002CV or 0,5µA whichever is greater ( after 2 minutes)	
-----------------------------	--	--

<b>Capacitance tolerance</b>	± 20% at 120Hz, 20°C											
<b>Dissipation factor max. (at 120Hz, 20°C)</b>		WV										
	Tanδ	Ø4 ~ Ø6,3mm	6,3	10	16	25	35	50				
			0,24	0,20	0,16	0,14	0,12	0,10				

<b>Low Temperatur characteristics ( Impedance ratio at 120Hz )</b>		WV										
		Z-25°C / Z+20°C	6,3	10	16	25	35	50				
		Z-40°C / Z+20°C	4	3	2	2	2	2				
			8	6	4	4	3	3				

<b>Load Life ( after application of the rated voltage for 2000hrs at 85°C</b>	Leakage current	Less than specified value
	Capacitance Change	Within ±25% of initial value
	Tanδ	Less than 200% of specified value
	Ø4 ~ Ø6,3x5,4: 1000hours	

<b>CHIP Low Leakage Current</b>	
Part No.:	<b>I15004</b>

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014	Customer:
APPD:	Schumi			FINISH	Jamy		Sheet No.	2 from 7		

# EDCON-COMPONENTS



Designed for surface mounting on high density circuit board  
 Emboss carrier tape packing systems is available for automatic insertion



\_\_\_\_\_

<b>Resistance to soldering heat</b>	After reflow soldering and resistance at room temperature, they meet the characteristics requirements listed at underside	
	Leakage current	Less than specified value
	Capacitance Change	Within $\pm 10\%$ of initial value
	Tan $\delta$	Less than specified value

FREQUENCY COEFFICIENT OF PERMISSIBLE RIPPLE CURRENT					
Frequency	50Hz	120Hz	300Hz	1KHz	10KHz $\leq$
Coefficient	0,70	1,00	1,17	1,36	1,50

<b>CHIP Low Leakage Current</b>
Part No.: <b>I15004</b>
Customer:

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014
APPD:	Schumi			FINISH	Jamy		Sheet No.		3 from 7

# EDCON-COMPONENTS



Designed for surface mounting on high density circuit board

Emboss carrier tape packing systems is available for automatic insertion

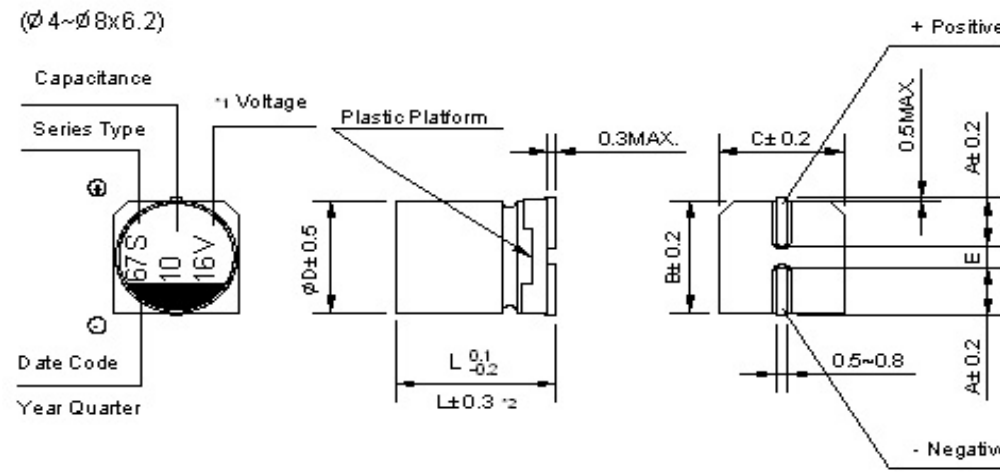


## Technical Drawing

### Dimension of Size $\varnothing 8 \times 10,5 \sim 16 \text{mm}$



### Dimension of Size $\varnothing 4 \sim 8 \times 6,2 \text{mm}$



D x L	$\varnothing 4 \times 5,4$	$\varnothing 5 \times 5,4$	$\varnothing 6,3 \times 5,4$	$\varnothing 6,3 \times 7,7$						
A	2,0	2,2	2,6	2,6						
B	4,3	5,3	6,6	6,6						
C	4,3	5,3	6,6	6,6						
E +/-0.2	1,0	1,3	1,9	1,9						
L	5,4	5,4	5,4	7,7						

<sup>1</sup> Voltage mark (6V) represents 6,3V for  $\varnothing 4 \sim 10 \text{mm}$

<sup>3</sup> (L +/- 0.5) is applicable to  $\varnothing 8 \times 10,5 \sim \varnothing 10 \text{mm}$

<sup>2</sup> (L +/- 0.3) is applicable to  $\varnothing 6,3 \sim 7,7$  and  $\varnothing 8 + 6,2 \text{mm}$

<sup>4</sup> (L +/- 1.0) is applicable to  $\varnothing 12,5 \sim \varnothing 16 \text{mm}$

RE. Date code and seriew type -1st digit for Year 2nd digit for Quarter, 4 quarter codes in one year area 1,4,7,0

3rd character for Serie S

**CHIP Low Leakage Current**

Part No.: **I15004**

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014	Customer:
APPD:	Schumi			FINISH	Jamy		Sheet No.	4 from 7		

[www.edcon-components.com](http://www.edcon-components.com)

email: [info@edcon-components.com](mailto:info@edcon-components.com)

# EDCON-COMPONENTS



Designed for surface mounting on high density circuit board

Emboss carrier tape packing systems is available for automatic insertion



Voltage Ordering Code	Code
4,0	<b>4V0</b>
6,3	<b>6V3</b>
10	<b>100</b>
16	<b>160</b>
25	<b>250</b>
35	<b>350</b>
50	<b>500</b>
63	<b>630</b>
80	<b>800</b>
100	<b>101</b>

Diameter ordering Code D	Code
3mm	<b>3</b>
4mm	<b>A</b>
5mm	<b>B</b>
6,3mm	<b>C</b>
8mm	<b>E</b>
10mm	<b>G</b>
12,5mm	<b>I</b>
16mm	<b>K</b>

Height ordering Code L	Code
4mm	<b>A</b>
5,4mm	<b>0</b>
5,8mm	<b>1</b>
6,2mm	<b>2</b>
7,7mm	<b>3</b>
10,5mm	<b>4</b>
13,5mm	<b>5</b>
16mm	<b>6</b>
16,5mm	<b>7</b>

<b>CHIP Low Leakage Current</b>	
Part No.:	<b>I15004</b>
Customer:	

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014
APPD:	Schumi			FINISH	Jamy		Sheet No.	5 from 7	

# EDCON-COMPONENTS



Designed for surface mounting on high density circuit board

Emboss carrier tape packing systems is available for automatic insertion



## Ordering Informations

Serie	Voltage Code	Tolerance Code	Range Code	Size Code D	Size Code L	Special function	ROHS	Packing Code		
-------	--------------	----------------	------------	-------------	-------------	------------------	------	--------------	--	--

<b>I15004</b>	<b>6V3</b>	<b>M</b>	<b>220</b>	<b>A</b>	<b>0</b>	<b>XX</b>	<b>R</b>	<b>TR</b>		
---------------	------------	----------	------------	----------	----------	-----------	----------	-----------	--	--

look table	<b>M= ±20%</b>	Ordering Code Range	Look table Code D	Look table Code L	<b>XX= No function</b>	<b>R= ROHS Conform</b>	<b>TR= Tape Reel Packing</b>		
Voltage Code						<b>N= NON ROHS Conform</b>			
							<b>BU= Bulk-Ware</b>		

**CHIP Low Leakage Current**

Part No.: **I15004**

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014
APPD:	Schumi			FINISH	Jamy		Sheet No.		6 from 7

Customer:

# EDCON-COMPONENTS



Designed for surface mounting on high density circuit board  
 Emboss carrier tape packing systems is available for automatic insertion



## Soldering Profile Curve

### Classification Reflow Profile (JEDEC J-STD-020C)



<b>CHIP Low Leakage Current</b>	
Part No.:	<b>I15004</b>
Customer:	

DRW:	Jason	CHKD	Wilson	MATL:	Wilson	TOLERANCE	Mason	DATE	03.06.2014
APPD:	Schumi			FINISH	Jamy		Sheet No.	7 from 7	